

REMARKS

Reconsideration of this application, as amended, is respectfully requested. The office action objected to the drawings because figure 1 should be labeled as prior art, figure 4 contains two I_{cap} annotations and these two do not represent the same thing, figure 4 is not a transistor representation of figure 3, and the drawings do not show a predriver coupled between the capacitor and the first driver amplifier. The Examiner has acknowledged that claims 1-19, and 24-26 are directed to allowable subject matter.

In the specification, the paragraphs have been amended to correct minor editorial problems as well as references to I_{cap1} and I_{cap2} of Figure 4. In amended Figure 1, the label "Prior Art" has been added. Figure 4, as amended, changes the " I_{cap} " annotation to I_{cap1} and I_{cap2} . In the claims, claim 12 has been amended to state "a pre-driver amplifier coupled to the capacitor and the first driver amplifier."

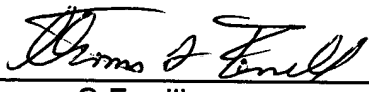
In contrast to the Office Action's assertions, Figure 4 may be an equivalent transistor level circuit of Figure 3. However, the amended description of Figure 4 makes any argument moot. In contrast to the Office Action's assertions, the figures, such as Figure 6, do illustrate multiple predriver amplifiers, driver amplifiers, and current mirrors. However, amended claim 12 makes any argument moot.

The applicant submits that the present application with claims 1-19 and 24-26 are in condition for allowance. Applicant respectfully requests that a timely Notice of Allowance be issued in this case. If the Examiner has any questions, please feel free to call me at 408-720-8300. Please charge any shortages and credit any overcharges to our Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 7-23, 2003



Thomas S Ferrill
Registration No. 42,532

12400 Wilshire Boulevard
Seventh Floor
Los Angeles, CA 90025-1026
(408) 720-8300